



# Driver circuit for Si photodiode array

C9004-01

## Driver circuit for 16-element photodiode array

#### 📮 Features

- High precision and high-speed measurement by simultaneous 16-channel readout
- Assembled with pulse generator (8-step adjustable oscillatory frequency) CLK, START, A/D conversion Trig and EOS pulse output
- Choice of gain (conversion impedance): 1 × 10<sup>6</sup> or 1 × 10<sup>7</sup> (V/A)
- Hamamatsu S4111-16 series, S11212 series photodiode arrays are directly mountable on board.
- Single power supply operation: +12 V

#### - Applications

- Performance evaluation of Hamamatsu S4111-16 series, S11212 series photodiode arrays
- Position measurement
- Displacement measurement

#### Absolute maximum ratings (Ta=25 °C, unless otherwise noted)

Parameter	Symbol	Value	Unit
Supply voltage	Vs max	+13	V
Photodiode array input current	Ipin max	+125.5 × 10 <sup>-5</sup>	А
Supply current	Iin max	2	Α
Operating temperature*1	Topr	0 to +50	°C
Storage temperature*1	Tstg	-20 to +80	°C

\*1: No dew condensation

When there is a temperature difference between a product and the surrounding area in high humidity environment, dew condensation may occur on the product surface. Dew condensation on the product may cause deterioration in characteristics and reliability.

Note: Exceeding the absolute maximum ratings even momentarily may cause a drop in product quality. Always be sure to use the product within the absolute maximum ratings.

2-	Electrical a	nd optical	characteristics	(Ta=25 °C	)
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Parameter	Symbol	Condition		Min.	Тур.	Max.	Unit
Teres at the state of successful	Ip	$Zt=1 \times 10^{6}$		-	6.4 × 10 <sup>-6</sup>	-	A
Input photocurrent		$Zt=1 \times 10^{7}$		-	6.4 × 10 <sup>-7</sup>	-	A
Conversion impedance*2	Zt			-	$1 \times 10^{6}$	-	V/A
Conversion impedance*2	Ζl			-	1 × 10 <sup>7</sup>	-	V/A
Output offset voltage	Vos	$Zt=1 \times 10^{6}$ (set up prior to shipping)		-	0.025	-	v
		$Zt=1 \times 10^7 *^3$		-	0.25	-	V
Maximum output amplitude voltage	Vfs	$Zt=1 \times 10^6$ , RL=1 k $\Omega$		+6.4	-	-	V
		$Zt=1 \times 10^7$ , RL=1 k $\Omega$		+6.4	-	-	V
Output noise voltage	Vn	$Zt=1 \times 10^{6}$ (full bandwidth)		-	5	-	mVp-p
		$Zt=1 \times 10^7$ (full bandwidth)		-	10	-	mVp-p
	fc		Lower	-	DC	-	- kHz
Cutoff frequency			Upper	-	62.4	-	
		$Zt=1 \times 10^{7}$ ,	Lower	-	DC	-	КПД
		RL=1 kΩ, -3 dB	Upper	-	62.4	-	
Capacitive load	CL			-	-	100	pF
Oscillatory frequency (OUT)*4	CLK			1.5625	-	200	kHz
Start pulse width (OUT)*4	-			5	-	640	μs
Output format*5	-			πι		-	
Operating supply voltage	Vs	*6		+11.5	+12	+12.5	V
Current consumption	Is			-	200	250	mA

\*2: Conversion impedance can be changed with the switch on the circuit board.

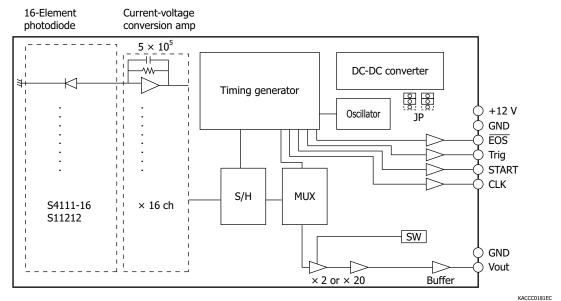
\*3: The variable resistor V<sub>R</sub> on the circuit board must be used for making offset adjustments.

\*4: Adjustable in 8 steps by using the BCD rotary switch on the circuit board

\*5: CLK, START, Trig and EOS pulse output format

\*6: A power supply of approximately 12 V and 1.25 A is recommended. The electric current for operating this product varies depending on the use environment. Please check in advance.

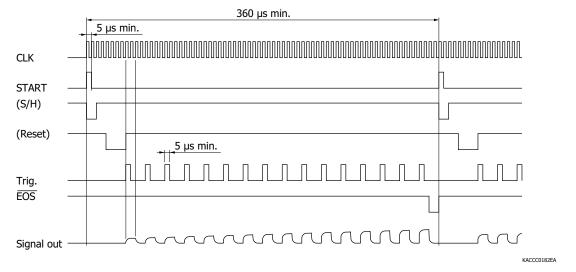
#### Block diagram



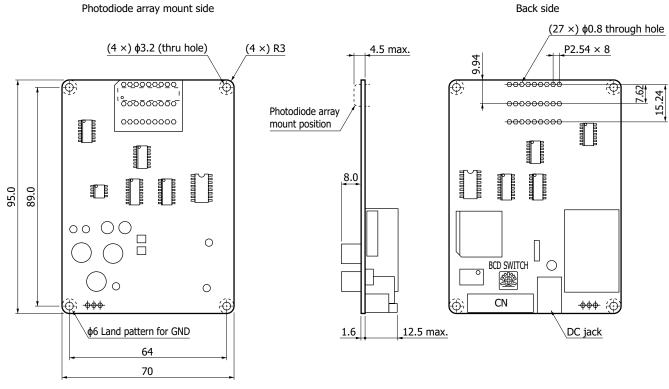
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## Driver circuit for Si photodiode array

## Timing chart



## Dimensional outline (unit: mm)



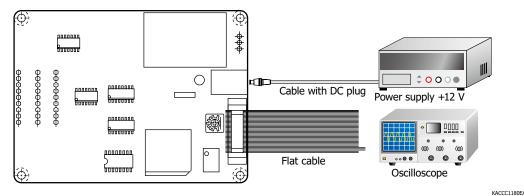
Note: Photodiode array sold separately

Tolerance unless otherwise noted: ±0.3

KACCA0489EA



### Connection example



#### Accessories

- · Instruction manual
- · Cable with DC plug
- · Flat cable (200 mm) with I/O connector receptacle

#### Related information

www.hamamatsu.com/sp/ssd/doc\_en.html

- Precautions
- Disclaimer

Information described in this material is current as of January 2023.

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