

To: "Steve Andrus" <sandrus@awaretech.com>
From: Mike Pitsakis <mpitsakis@awaretech.com>
Subject: RE: USB chip
Cc:
Bcc:
Attached:

It relates to reliably programming the USB chip. I wish it fixed the com hanging issue too but don't know.
Mike

At 04:04 PM 6/17/2011, you wrote:

If this relates to the hanging problem during FW loading than I would think we need to implement as soon as reasonably possible. I don't think I would open Boxes that are in shipping but perhaps the instruments that are in assembly

From: Mike Pitsakis [<mailto:mpitsakis@awaretech.com>]
Sent: Friday, June 17, 2011 3:52 PM
To: sandrus@awaretech.com
Subject: Fwd: USB chip

Steve,

Should we incorporate these changes into our current production schedule and update all board? It affects 42xx, 45xx, 47xx.

Mike

X-Mailer: QUALCOMM Windows Eudora Version 7.0.1.0
Date: Thu, 16 Jun 2011 12:05:30 -0400
To: Mike Pitsakis <mpitsakis@awaretech.com>
From: Mike Reynolds <mreynolds@awaretech.com>
Subject: USB chip
X-AWARETECH: Whitelist1

Mike,

We found a solution to programming the USB chip. Two modifications are to change the oscillator capacitors to 68pF and pulling up RXD and CTS. RXD and CTS are output through a CMOS '244 buffer (on the eZ80 PCB) so the pullups do not make a lot of sense but that is what FTDI recommends.

I alerted Graham so that he can see if it fixes some of his issues.

Mike Reynolds
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