



February 1999

FDC6330L

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Integrated Load Switch

General Description

This device is particularly suited for compact power management in portable electronic equipment where 3V to 20V input and 2.3A output current capability are needed. This load switch integrates a small N-Channel power MOSFET (Q1) which drives a large P-Channel power MOSFET (Q2) in one tiny SuperSOT™-6 package.

Applications

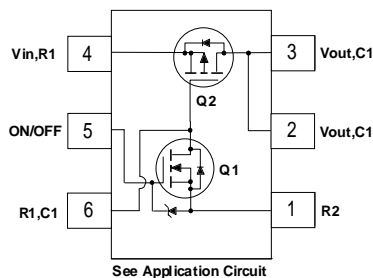
- Power management
- Load actuation

Features

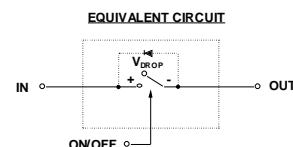
- $V_{\text{DROP}} = 0.2\text{V} @ V_{\text{IN}} = 12\text{V}, I_{\text{L}} = 2.5\text{A}, R_{\text{(ON)}} = 0.08\ \Omega$
 $V_{\text{DROP}} = 0.2\text{V} @ V_{\text{IN}} = 5\text{V}, I_{\text{L}} = 1.6\text{A}, R_{\text{(ON)}} = 0.125\ \Omega$
- Control MOSFET (Q1) includes Zener protection for ESD ruggedness (>6kV Human Body Model).
- High performance PowerTrench™ technology for extremely low on-resistance.
- SuperSOT™-6 package design using copper lead frame for superior thermal and electrical capabilities.



SuperSOT™-6



See Application Circuit



Absolute Maximum Ratings T_A=25°C unless otherwise noted

Symbol	Parameter	Ratings	Units
V _{IN}	Input Voltage Range (Note 1)	3 - 20	V
V _{ON/OFF}	On/Off Voltage Range	1.5 - 8	V
I _D	Load Current - Continuous (Note 2)	2.3	A
		10	
P _D	Maximum Power Dissipation (Note 1)	0.7	W
T _J , T _{stg}	Operating and Storage Temperature Range	-55 to +150	°C
ESD	Electrostatic Discharge Rating MIL-STD-883D Human-Body-Model (100pf/1500 Ohm)	6	kV

Thermal Characteristics

R _{θJA}	Thermal Resistance, Junction-to-Ambient (Note 2)	180	°C/W
R _{θJC}	Thermal Resistance, Junction-to-Case (Note 2)	60	°C/W

Package Marking and Ordering Information

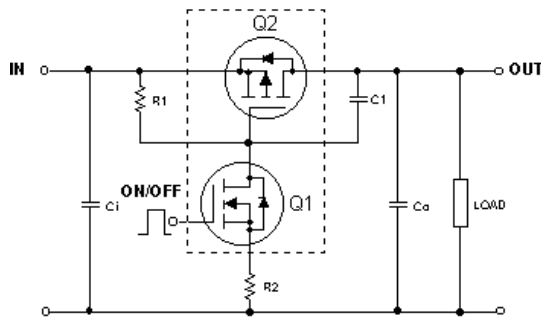
Device Marking	Device	Reel Size	Tape width	Quantity
.330 (. Denotes pin 1)	FDC6330L	7"	8mm	3000 units

Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
OFF Characteristics						
I_{FL}	Leakage Current	$V_{IN} = 20\text{ V}$, $V_{ON/OFF} = 250\text{ }\mu\text{A}$			1	μA
ON Characteristics (Note 3)						
V_{DROP}	Conduction Voltage	$V_{IN} = 12\text{ V}$, $V_{ON/OFF} = 3.3\text{ V}$, $I_L = 2.5\text{ A}$			0.2	V
		$V_{IN} = 5\text{ V}$, $V_{ON/OFF} = 3.3\text{ V}$, $I_L = 1.6\text{ A}$			0.2	V
$R_{(ON)}$	Q_2 - Static On-Resistance	$V_{GS} = -12\text{ V}$, $I_D = -2.3\text{ A}$ $V_{GS} = -5\text{ V}$, $I_D = -1.9\text{ A}$		0.054 0.081	0.08 0.125	Ω
I_L	Load Current	$V_{DROP} = 0.2\text{ V}$, $V_{IN} = 12\text{ V}$, $V_{ON/OFF} = 3.3\text{ V}$	2.5			A
		$V_{DROP} = 0.2\text{ V}$, $V_{IN} = 5\text{ V}$, $V_{ON/OFF} = 3.3\text{ V}$	1.6			

Notes:

1. Range of V_{IN} can be up to 30V, but R_1 and R_2 must be scaled such that V_{GS} of Q_2 does not exceed 20V.
2. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. $R_{\theta JC}$ is guaranteed by design while $R_{\theta JA}$ is determined by the user's board design.
3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2.0\%$.

FDC6330L Load Switch ApplicationAPPLICATION CIRCUIT**External Component Recommendation:**For applications where $C_o \leq 1\mu\text{F}$.For slew rate control, select R_2 in the range of $1\text{ k} - 4.7\text{ k}\Omega$.For additional in-rush current control, $C_1 \leq 1000\text{ pF}$ can be added.Select R_1 so that the R_1/R_2 ratio ranges from 10 - 100. R_1 is required to turn Q_2 off.

Typical Characteristics (continued)

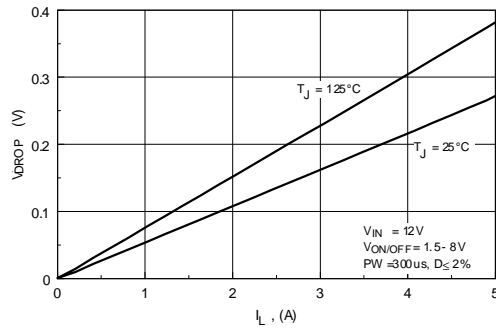


Figure 1. Conduction Voltage Drop Variation with Load Current.

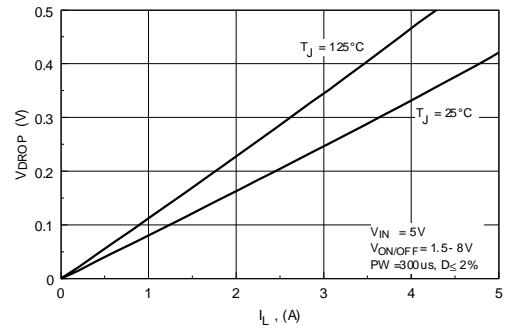


Figure 2. Conduction Voltage Drop Variation with Load Current.

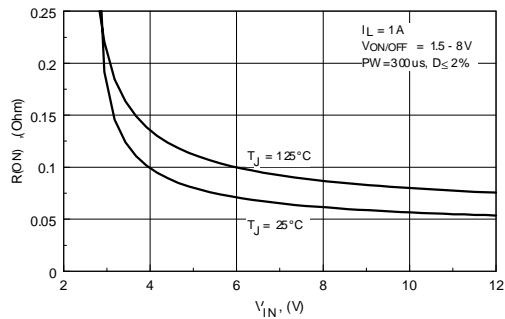


Figure 3. On-Resistance Variation with Input Voltage.

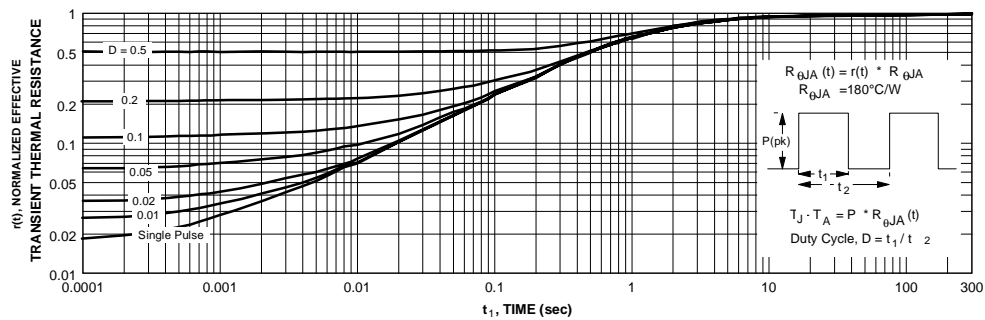


Figure 4. Transient Thermal Response Curve.

Thermal characterization performed using the conditions described in Note 2.
Transient thermal response will change depending on the circuit board design.

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